

DETAILED ACTION

1. Applicant's amendment dated March 30, 2009, responding to the Office action mailed December 30, 2008 provided in the rejection of claims 1-9, 11-16, and 18-24 wherein claims 1, 8, 14, and 19 have been further amended via an Examiner amendment.
2. Authorization for this examiner's amendment was given in a telephone interview with Mr. Cygan (Reg. No. 50,937) on June 1, 2009 to obviate any potential 35 U.S.C 112, second paragraph issues, and to place the claims in the condition for allowance.
3. The application has been amended as follows:

IN THE CLAIMS,

Please amend claims 1, 8, 14 and 19 as follows:

1. (Currently Amended)

At line 4, after “a write mask” by inserting “, wherein said write mask includes a bit corresponding to a component produced by said first instruction”; and

At line 7, after “previous link and a” by inserting “second” and after “write mask” by inserting “, wherein said second write mask includes a bit corresponding to a component produced by said second instruction”.

8. (Currently Amended)

At line 6, after “a write mask” by inserting “, wherein said write mask includes a bit corresponding to a component produced by said each instruction”.

14. (Currently Amended)

At line 9, after “a write mask” by inserting “, wherein said write mask includes a bit corresponding to a component produced by said first instruction”; and

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At line 12, after “previous link and a” by inserting “second” and after “write mask” by inserting “, wherein said second write mask includes a bit corresponding to a component produced by said second instruction”.

19. (Currently Amended)

At line 11, after “a write mask” by inserting “, wherein said write mask includes a bit corresponding to a component produced by said each instruction”.

- **END OF AMENDMENT** -

Allowable Subject Matter

4. Claims 1-9, 11-16, and 18-24 (renumbered as 1-22) are allowed.
5. The following is an examiner's statement of reasons for allowance:

As pointed out in REMARKS of the amendment, for examples, "Nair ... does not disclose ... *wherein the first instruction includes a previous link and a write mask ...*" (recited on page 11, last non-full paragraph); "... *determining, using said previous link of said at least one second instruction, if any components within a particular field of the at least one second instruction are required, wherein said previous link of said second instruction links said second instruction with a prior instruction that writes at least one component of said component ...*" (recited on page 12, second full paragraph); and "... *when no components of the at least one second instruction are required, deleting that at least one second instruction from the machine code; and when any component of the at least one second instruction is required, adding the at least one second instruction to the worklist in the memory.*" (recited on page 13)

Further, as pointed out by Applicant(s), the prior art of record fails to teach and/or suggest:

"... for static single assignment form dead code elimination ...

examining a first instruction ... includes a previous link and a write mask

...

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examining at least one second instruction ... includes a previous link and a write mask ...

determining ... if any components within a particular field ... required

... deleting the at least one second instruction

... adding the at least one second instruction ...”

, as recited in independent claim 1 and similarly recited in independent claims 8, 14, and 19.

6. Claims (2-7), (9, 11-13, 22), (15, 16, 18, 21, 23) and (20, 24) are considered allowable by virtue of their dependence on allowable independent claims 1, 8, 14 and 19 respectively.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ben C. Wang whose telephone number is (571) 270-1240. The examiner can normally be reached on 8:00-5:30 (EST/EDT), Monday through Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Ben C Wang/

Ben C. Wang

Examiner, Art Unit 2192

/Tuan Q. Dam/

Supervisory Patent Examiner, Art Unit 2192